	PAA132	Units
Blocking Voltage	60	V <sub>P</sub>
Load Current	600	mA
Max R <sub>ON</sub>	1	Ω

## **Features**

- Small 8-Pin Package
- TTL/CMOS Compatible
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- 3750V<sub>rms</sub> Input/Output Isolation
  No EMI/RFI Generation
- · Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Version Available

# **Applications**

- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
  - Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls
- Automotive

# **Description**

The dual 1-Form-A PAA132 Solid State Relay has two independent, optically coupled, normally open, single-pole relays in a single 8-pin package. Clare's patented OptoMOS architecture makes available the optically coupled technology necessary to activate the output's efficient MOSFET switches while providing a 3750V<sub>rms</sub> input-to-output isolation barrier. Control of the isolated output is accomplished by means of a highly effective GaAlAs infrared LED at the input.

By incorporating two independent single-pole relays into a single 8-pin package, the PAA132 saves board space by providing a more compact design solution than two discrete single-pole relays in a variety of applications.

# **Approvals**

- UL recognized: File Number E76270
- CSA Certified: File Number LR43639-10
- EN/IEC 60950-1 compliant

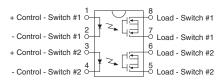
# **Ordering Information**

Part #		Description	
	PAA132	8 Pin DIP (50/Tube)	
	PAA132S	8 Pin Surface Mount (50/Tube)	
	PAA132STR	8 Pin Surface Mount (1,000/Reel)	

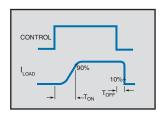
# **Pin Configuration**

#### PAA132 Pinout

AC/DC Configuration



# Switching Characteristics of Normally Open (Form A) Devices











# **Absolute Maximum Ratings (@ 25° C)**

Parameter	Ratings	Units
Blocking Voltage	60	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	Α
Input Power Dissipation 1	150	mW
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

# **Electrical Characteristics**

Parameter	Conditions	Symbol	Min	Тур	Max	Units			
Output Characteristics @ 25°C									
Load Current									
Continuous <sup>1</sup>	-	I <sub>L</sub>	-	-	600	mA			
Peak	t≤10ms	I <sub>LPK</sub>	-	-	2.0	А			
On-Resistance	I <sub>L</sub> =600mA	R <sub>on</sub>	-	0.85	1.0	Ω			
Off-State Leakage Current	V <sub>L</sub> =60V	I <sub>LEAK</sub>	-	-	1	μΑ			
Switching Speeds									
Turn-On	$I_F = 5mA, V_L = 10V$	T <sub>ON</sub>	-	-	5	ms			
Turn-Off		T <sub>OFF</sub>	-	-	2	ms			
Output Capacitance	$V_L = 50V, f=1MHz$	C <sub>OUT</sub>	-	25	-	pF			
Input Characteristics @ 25°C						'			
Input Control Current	$I_L = 600 \text{mA}$	I <sub>F</sub>	-	-	2	mA			
Input Dropout Current	-	I <sub>F</sub>	0.2	-	-	mA			
Input Voltage Drop	I <sub>F</sub> = 10mA	V <sub>F</sub>	0.9	1.2	1.4	V			
Reverse Input Current	$V_R = 5V$	I <sub>R</sub>	-	-	10	μΑ			
Common Characteristics @ 25°C	•		,	•		•			
Capacitance Input to Output	-	C <sub>I/O</sub>	-	3	-	pF			

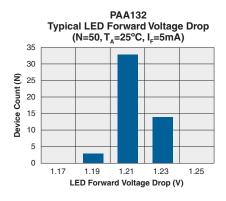
<sup>&</sup>lt;sup>1</sup> If both poles operate, the load current must be derated so that the package power dissipation value is not exceeded.

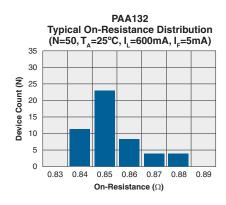
<sup>1</sup> Derate Linearly 1.33 mw / °C

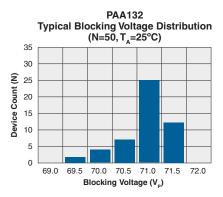
<sup>&</sup>lt;sup>2</sup> Derate Linearly 6.67 mw / °C

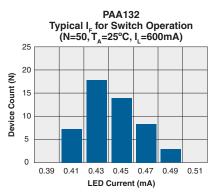


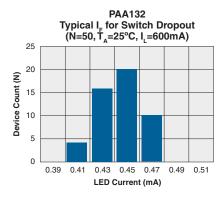
### **PERFORMANCE DATA\***

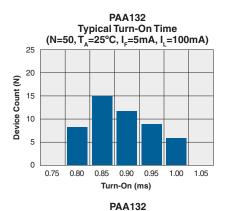


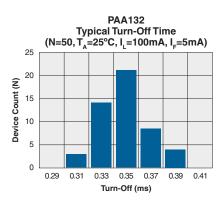


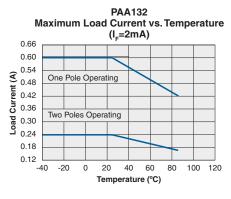


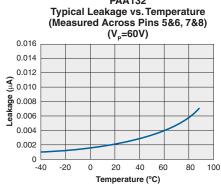


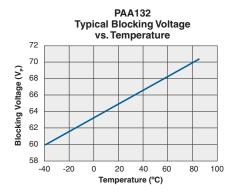


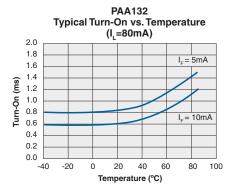


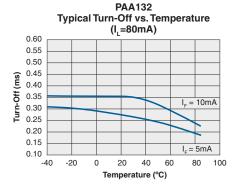








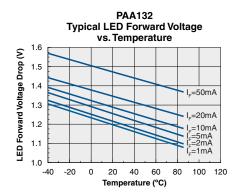


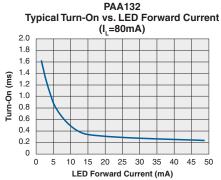


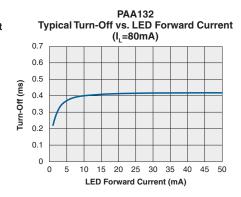
<sup>\*</sup>The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

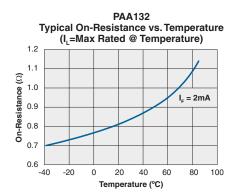


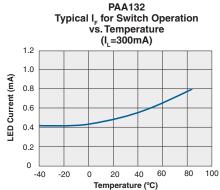
### PERFORMANCE DATA\*

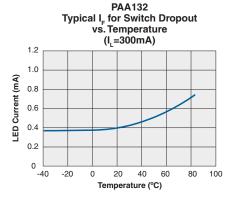


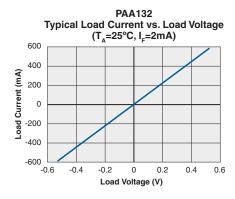


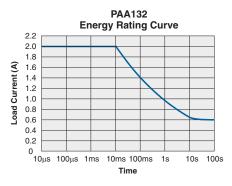












<sup>\*</sup>The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



# **Manufacturing Information**

# Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

# Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.







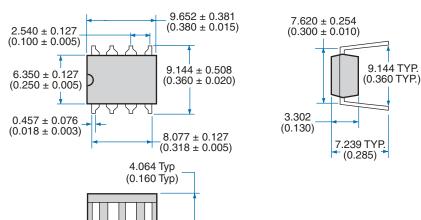
**PC Board Pattern** 

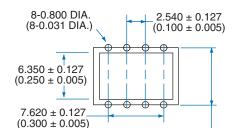
 $7.620 \pm 0.127$ 

 $(0.300 \pm 0.005)$ 

# **MECHANICAL DIMENSIONS**

# 8-Pin DIP Through-Hole Package





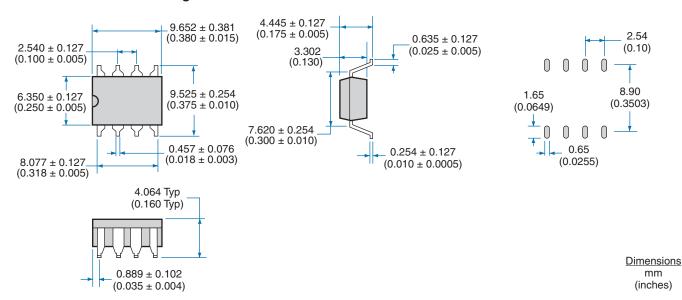
**Dimensions** mm (inches)

#### 8-Pin Surface Mount Package

 $0.889 \pm 0.102$ 

 $(0.035 \pm 0.004)$ 

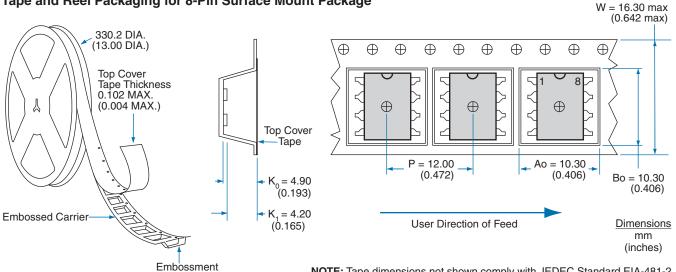
# **Recommended PCB Land Pattern**





### **MECHANICAL DIMENSIONS**

# Tape and Reel Packaging for 8-Pin Surface Mount Package



NOTE: Tape dimensions not shown comply with JEDEC Standard EIA-481-2

# For additional information please visit our website at: www.clare.com

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